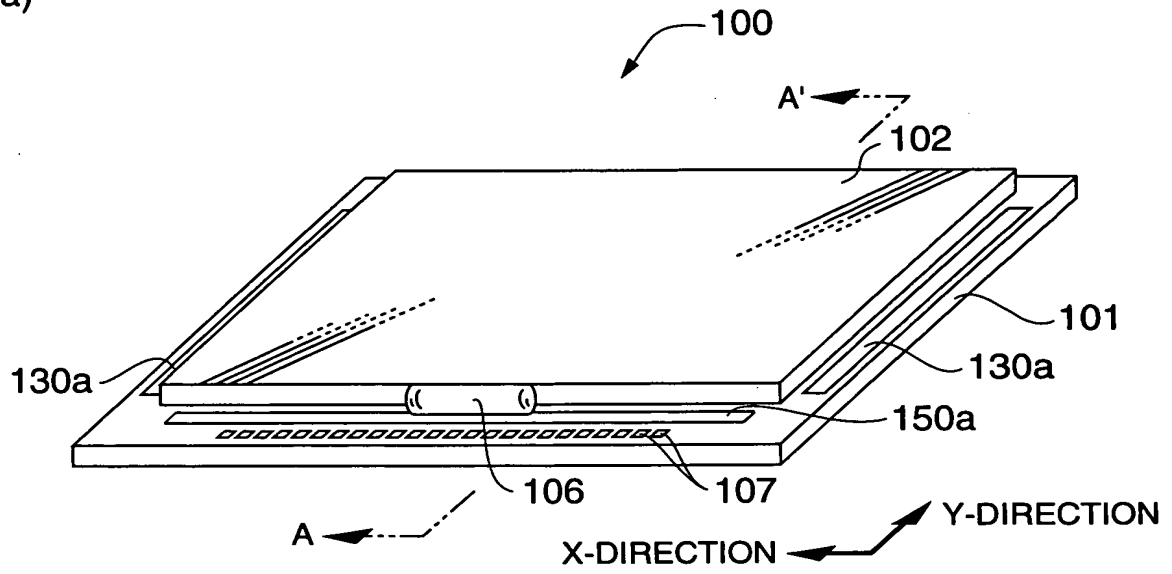


(a)



(b)

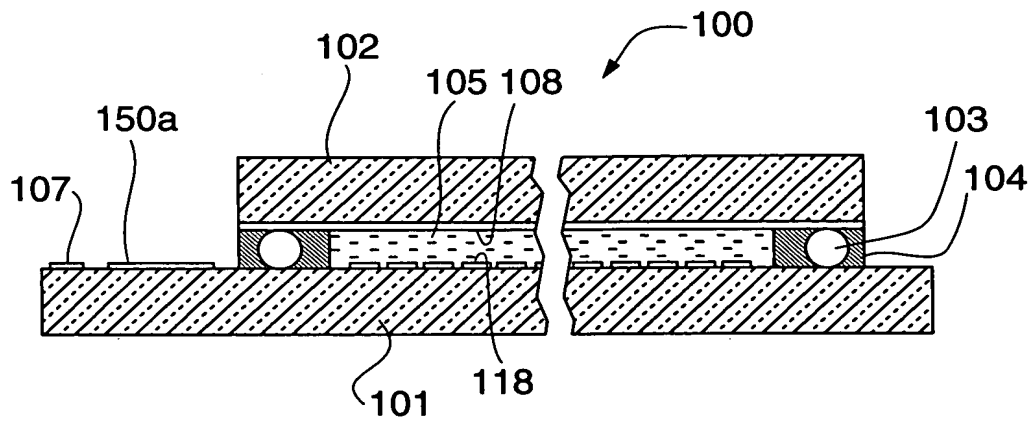


FIG. 1

10004822.120704

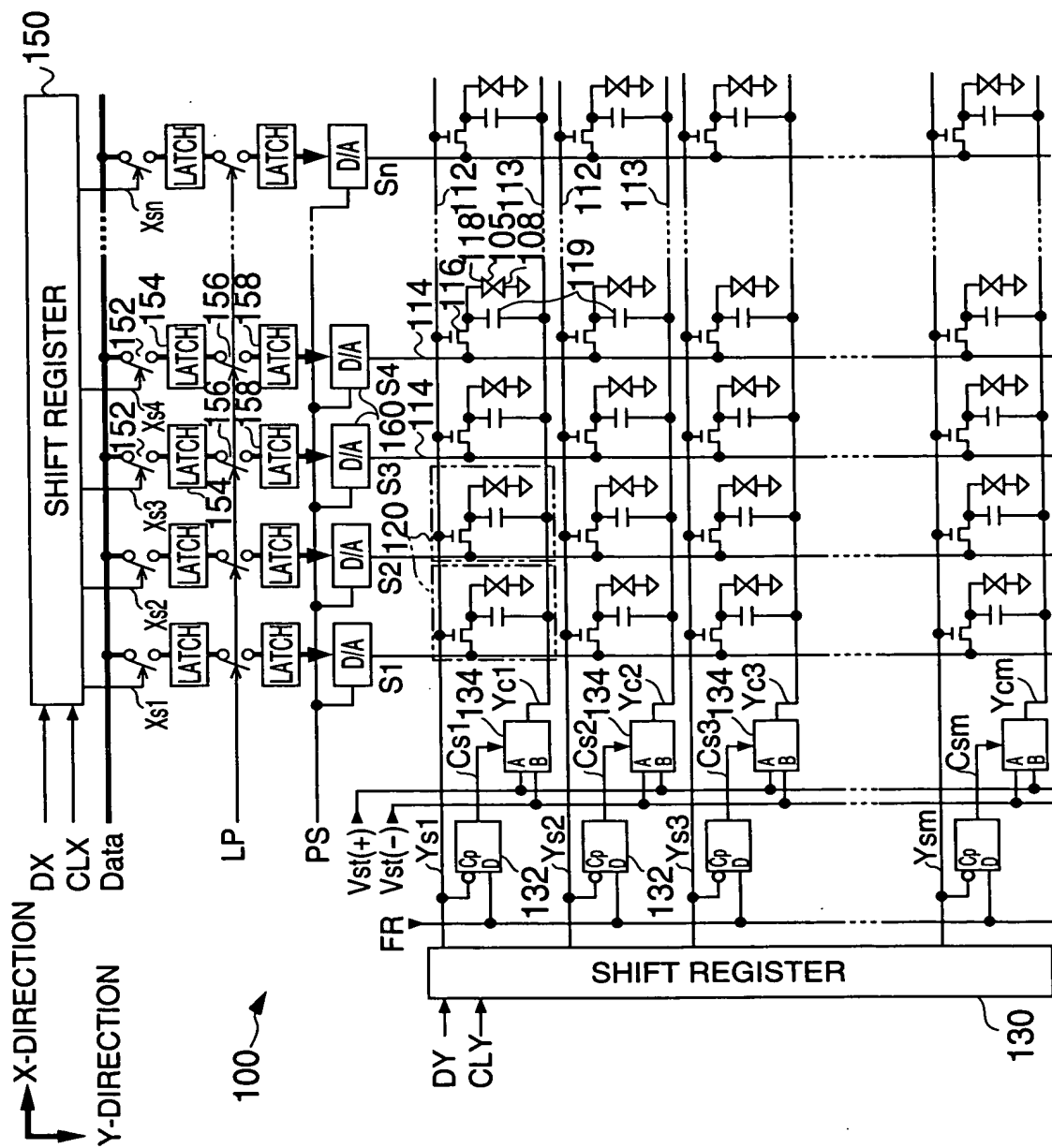


FIG. 2

10004622.120701

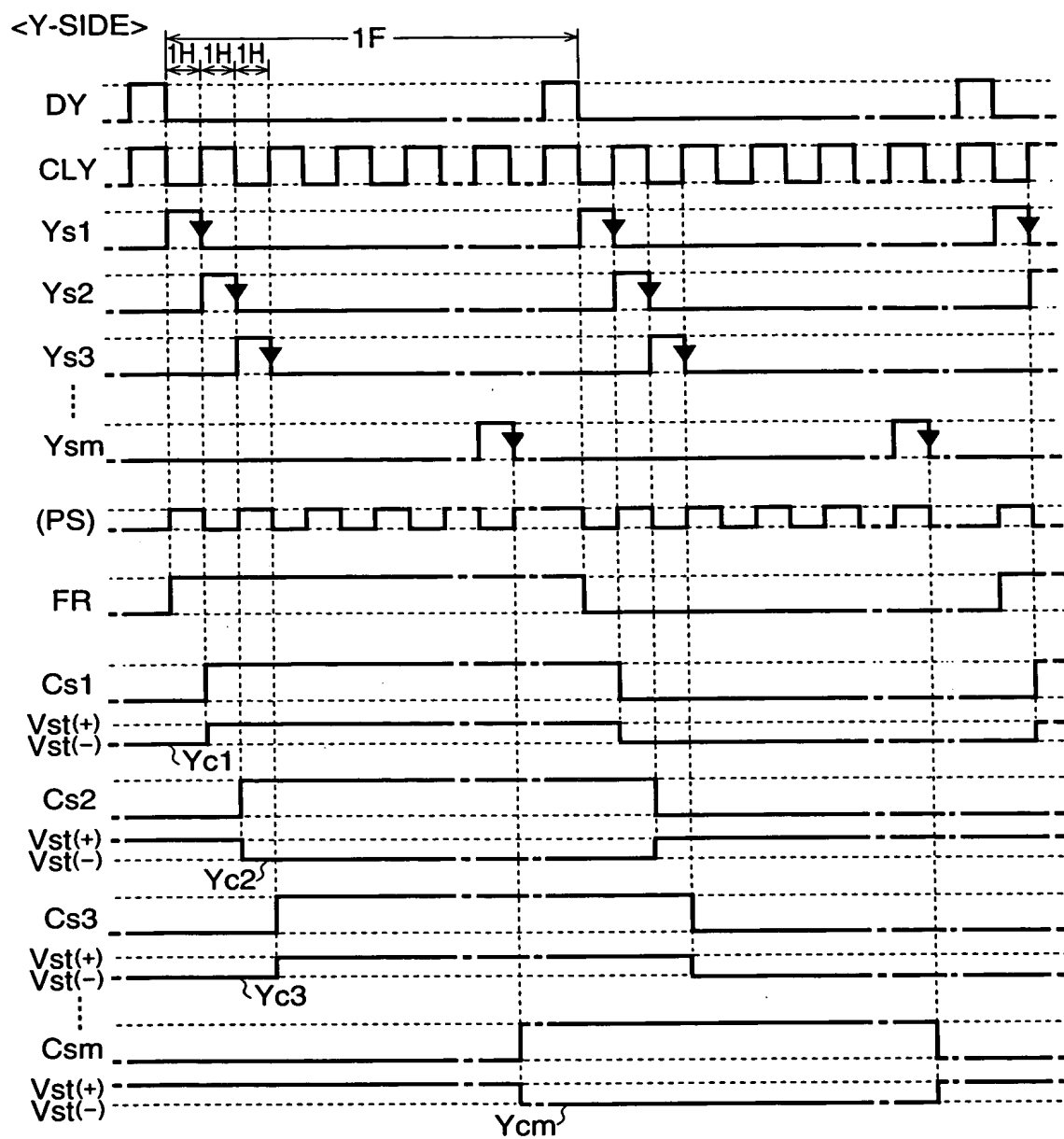


FIG. 3

<X-SIDE>

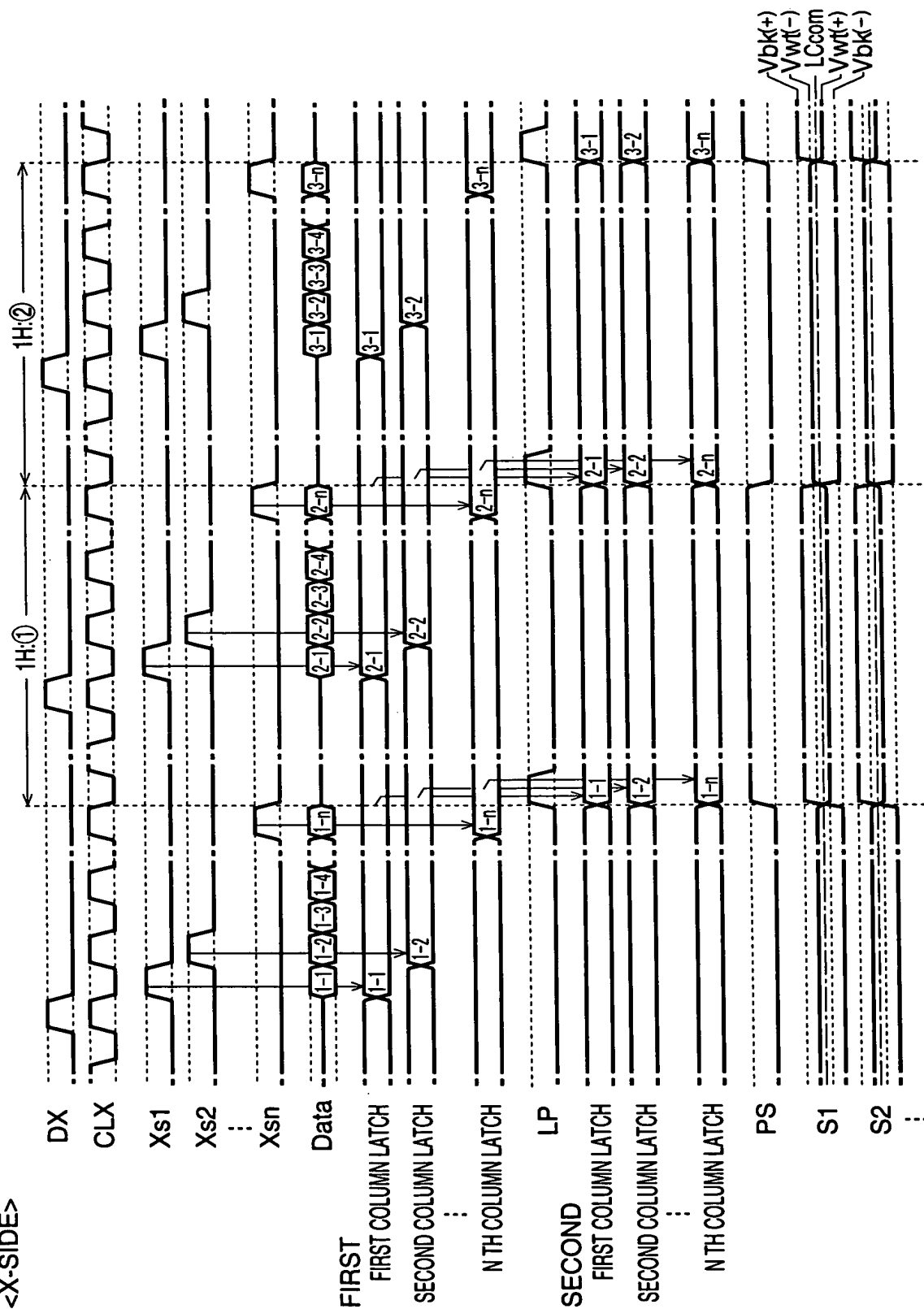
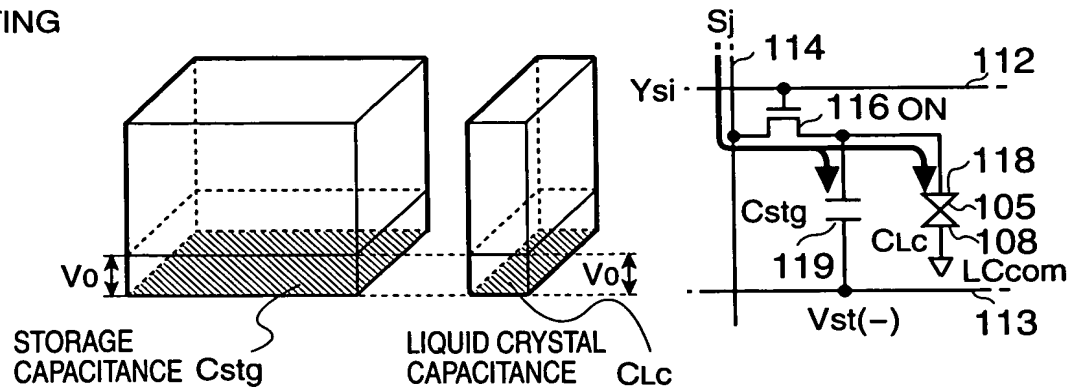
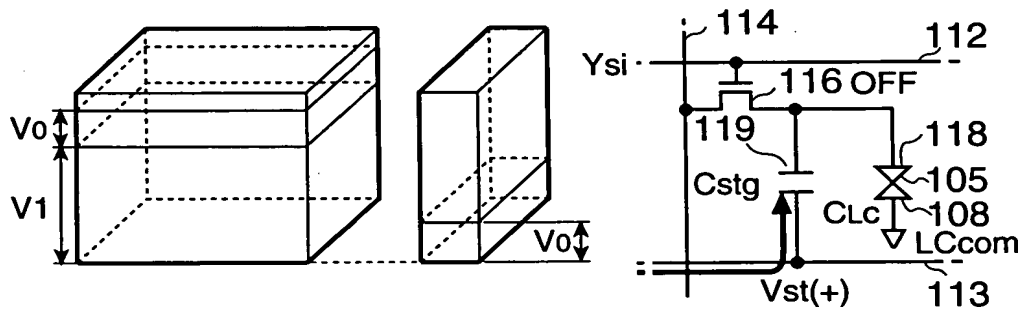


FIG. 4

(a) WRITING



(b) VOLTAGE SHIFT OF STORAGE CAPACITANCE



(c) CHARGE REDISTRIBUTION

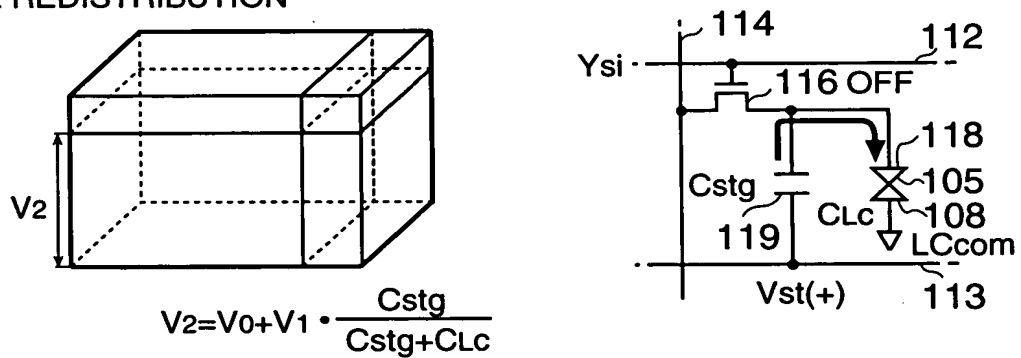


FIG. 5

10004622 120701

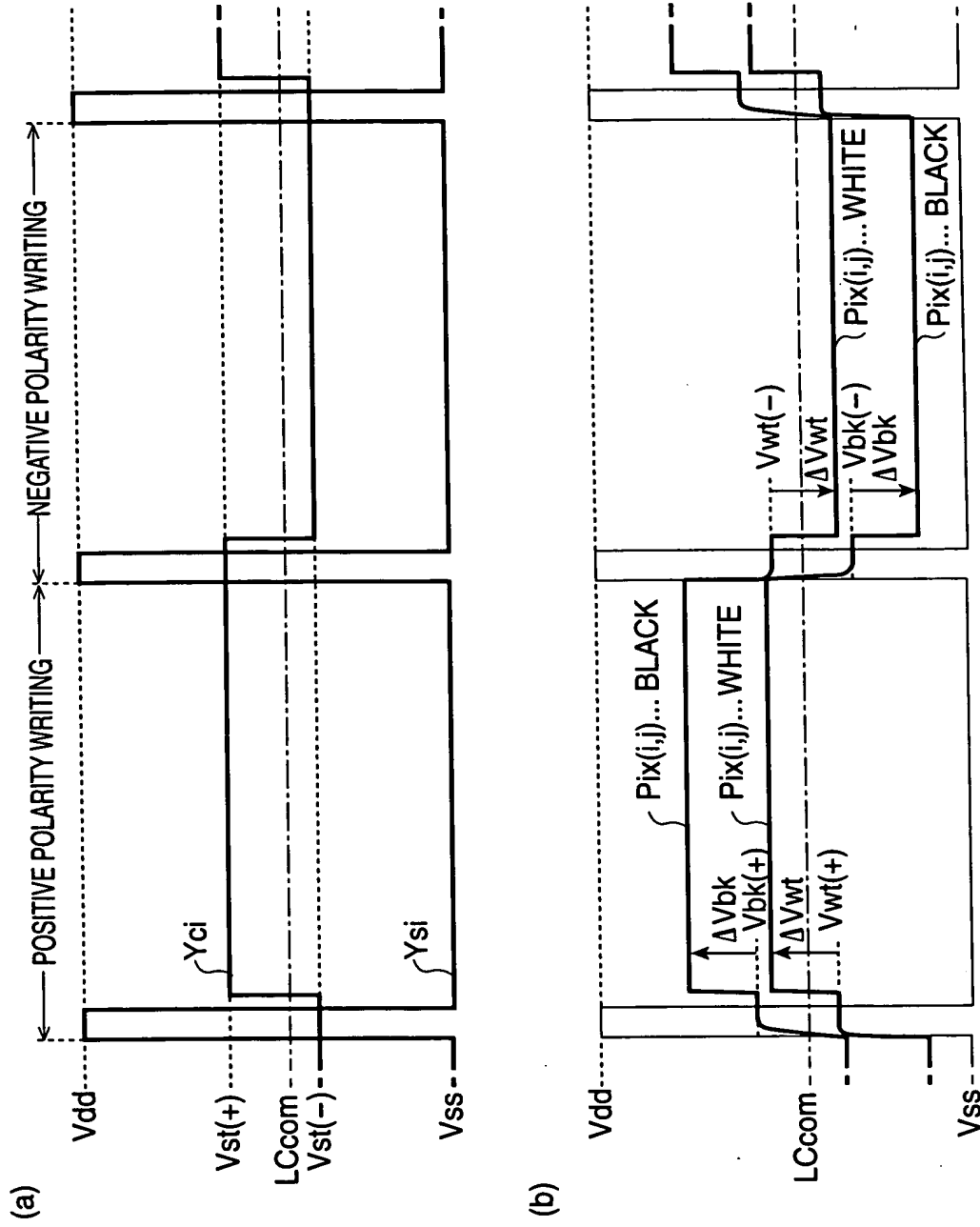


FIG. 6

10004622.120701

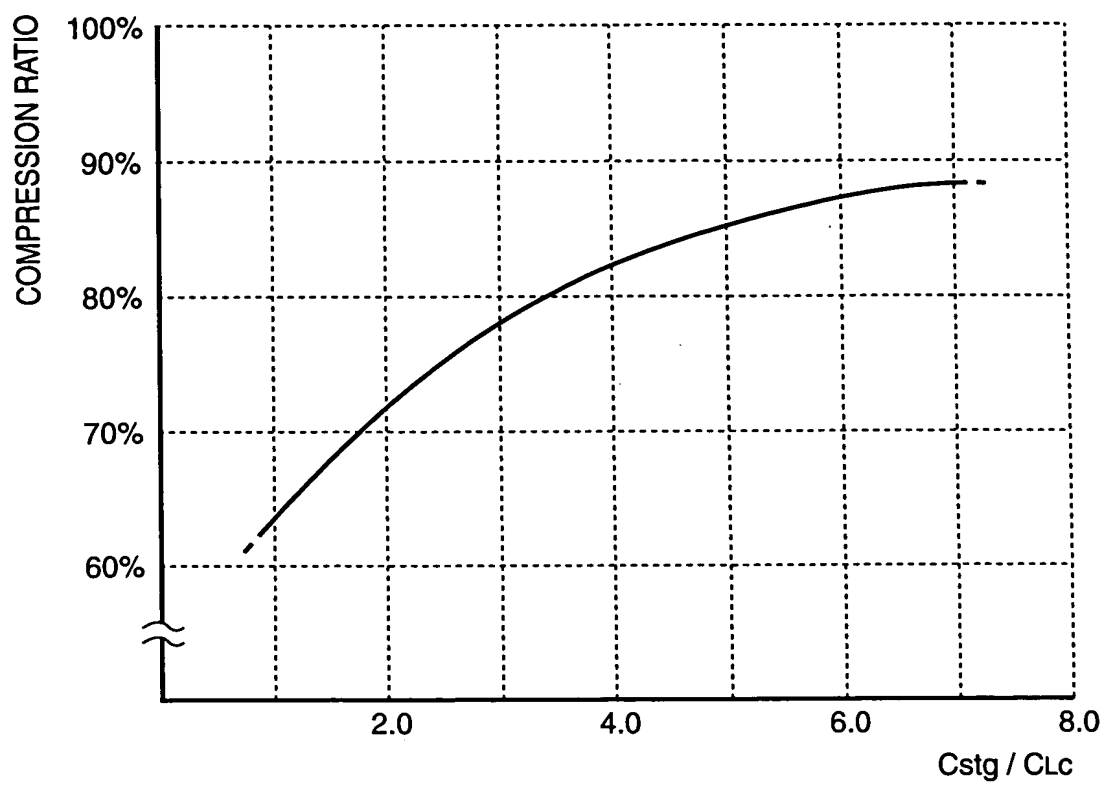


FIG. 7

10004822 120704

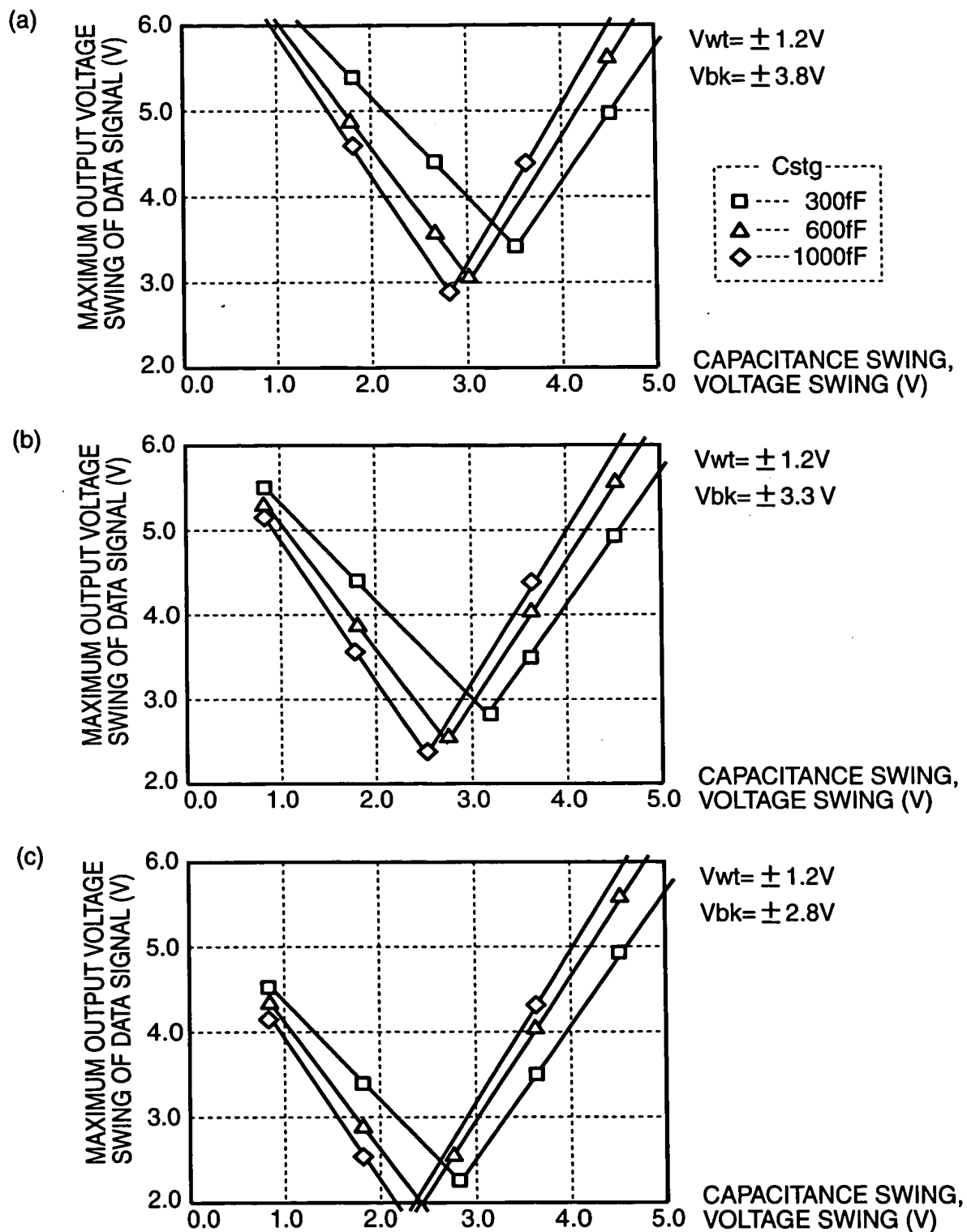


FIG. 8

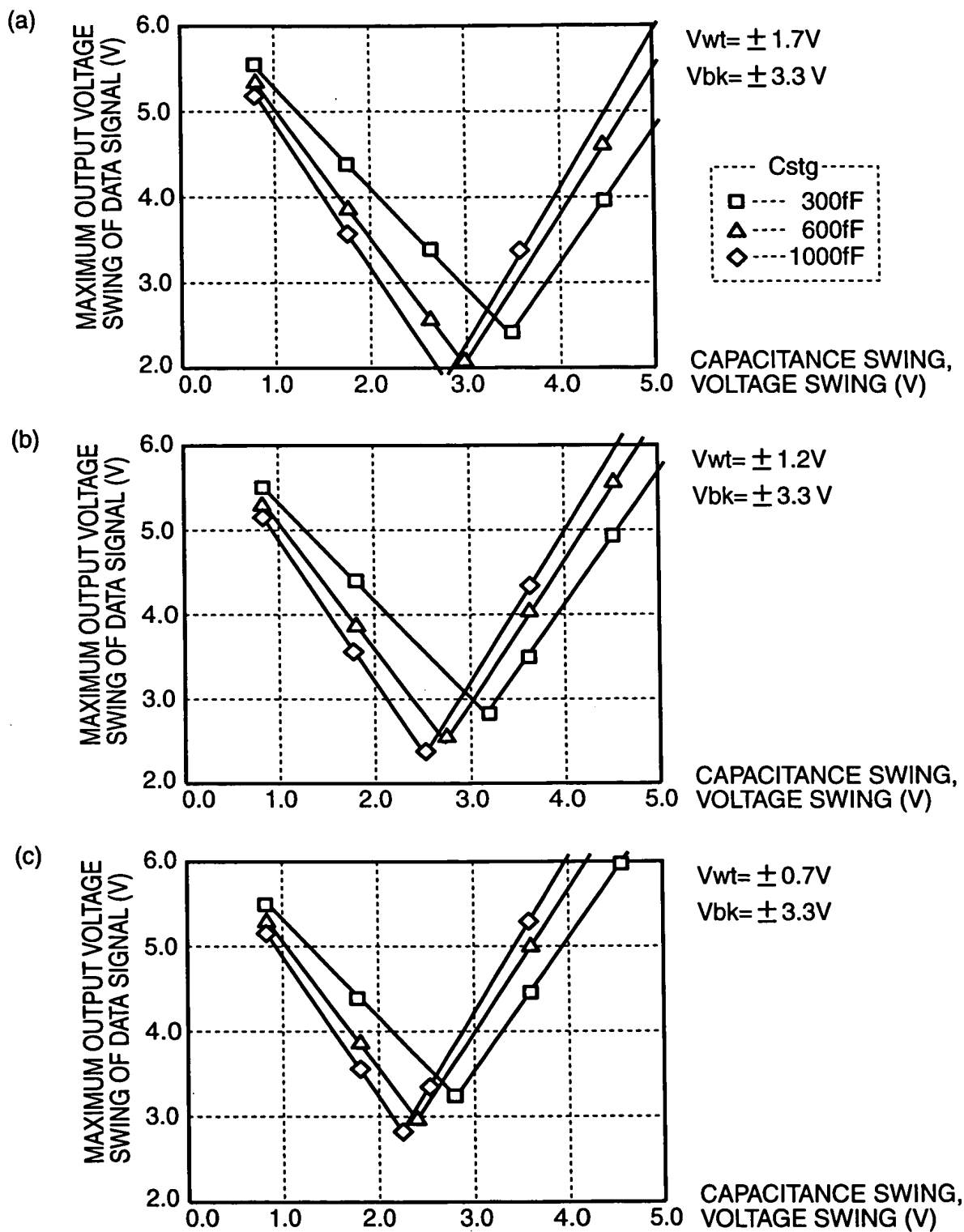


FIG. 9

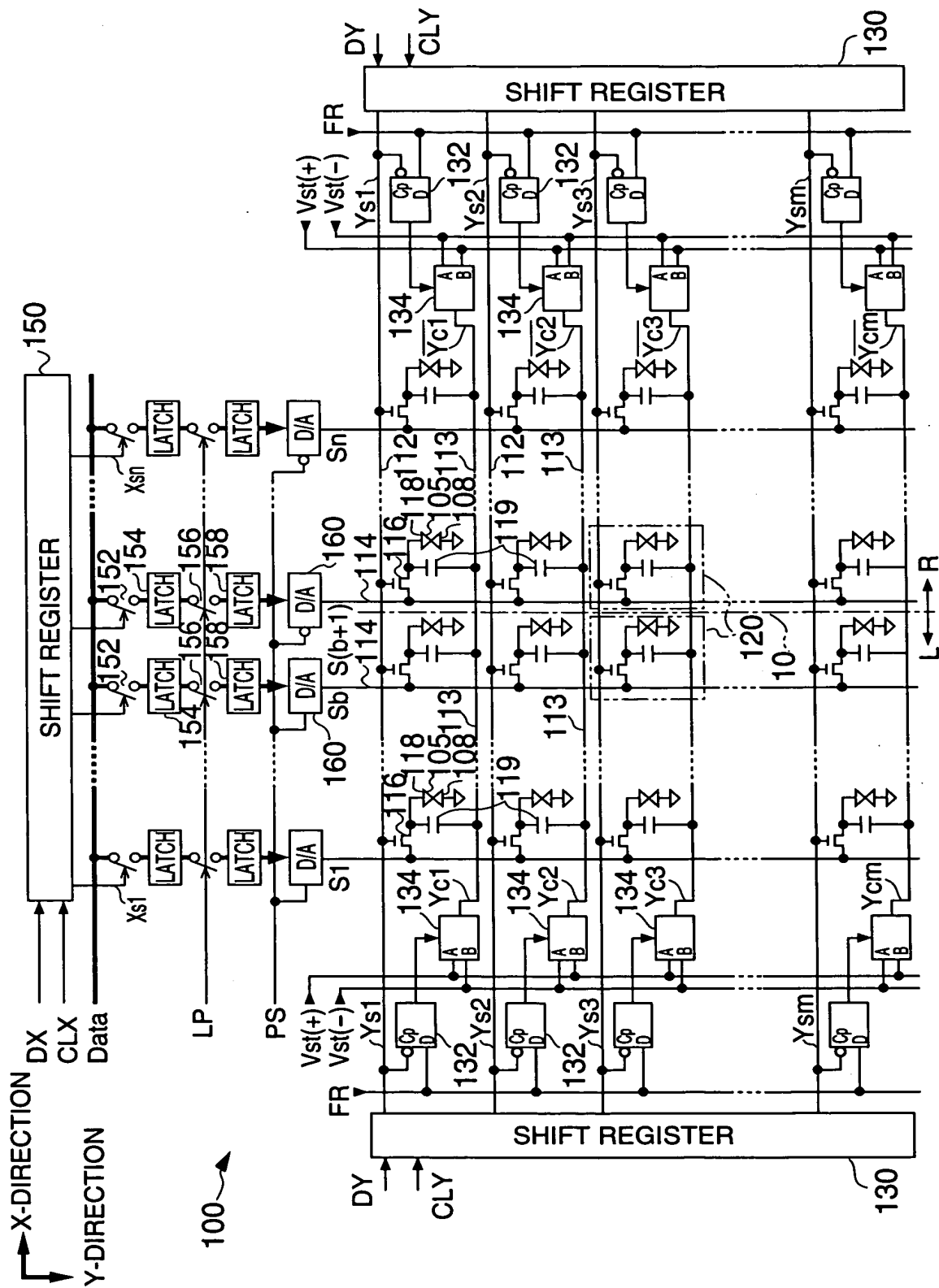


FIG. 10

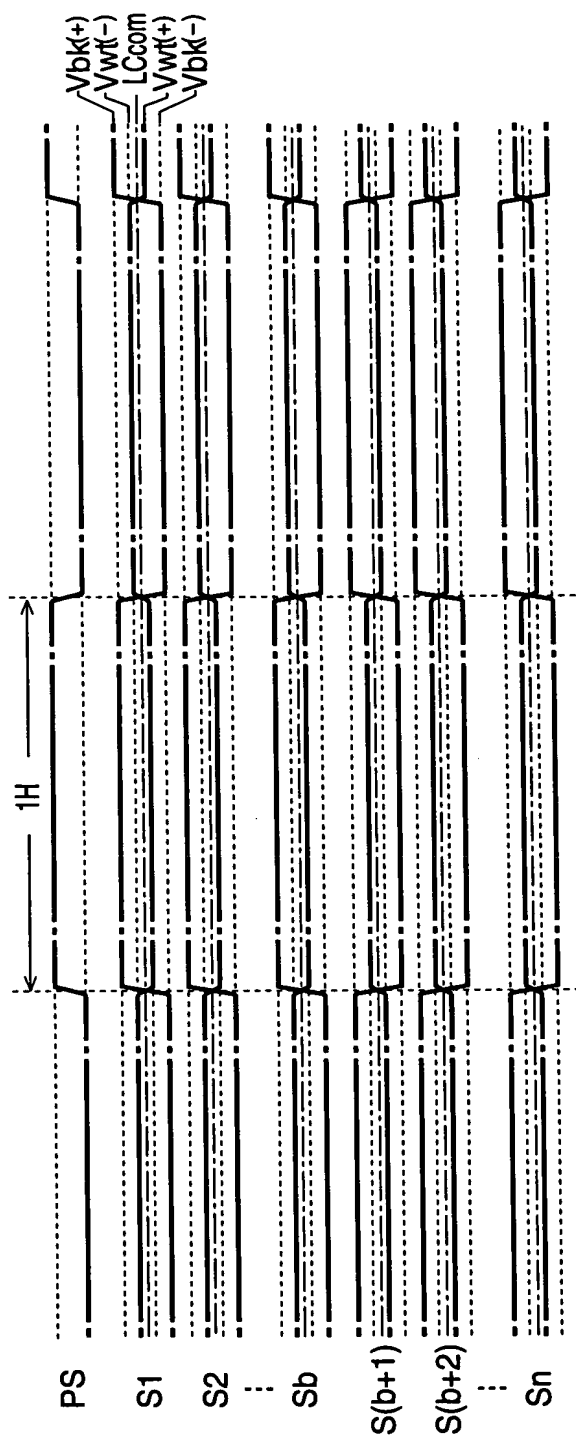


FIG. 11

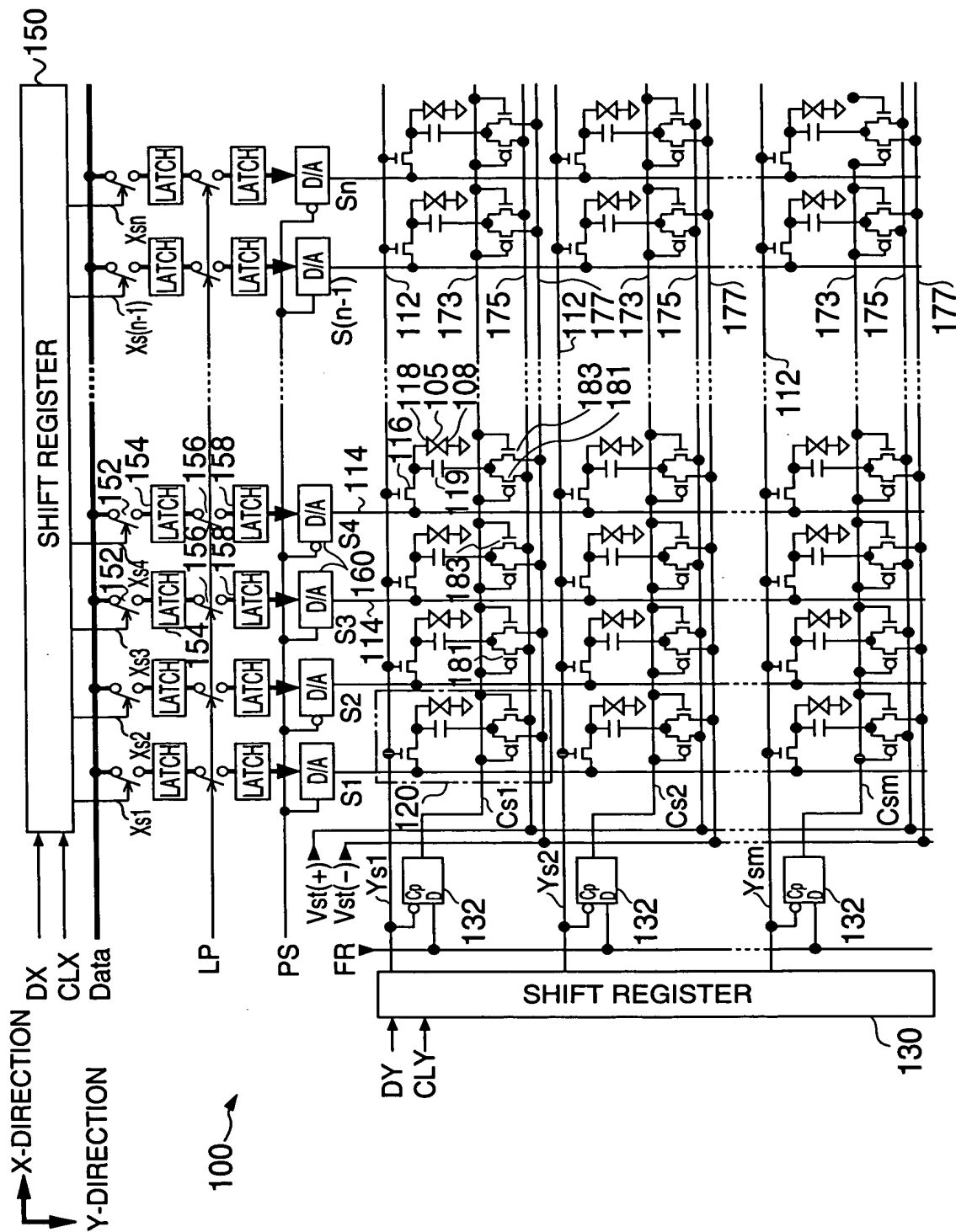


FIG. 12

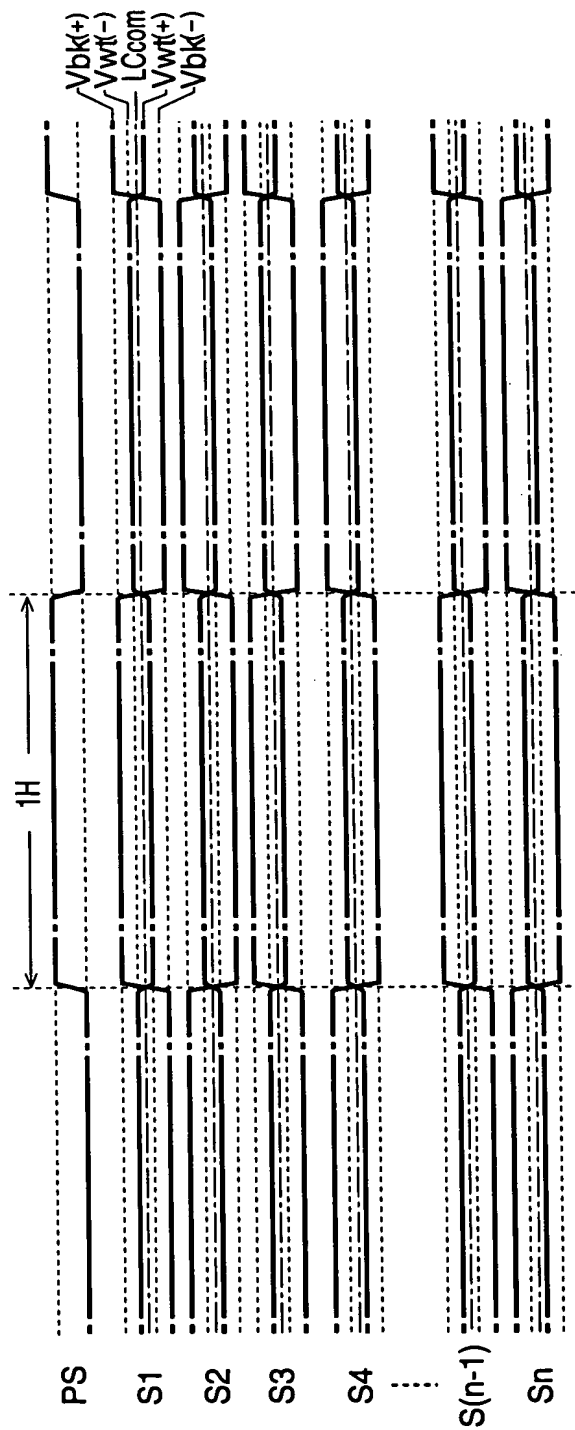


FIG. 13

1004622 120701

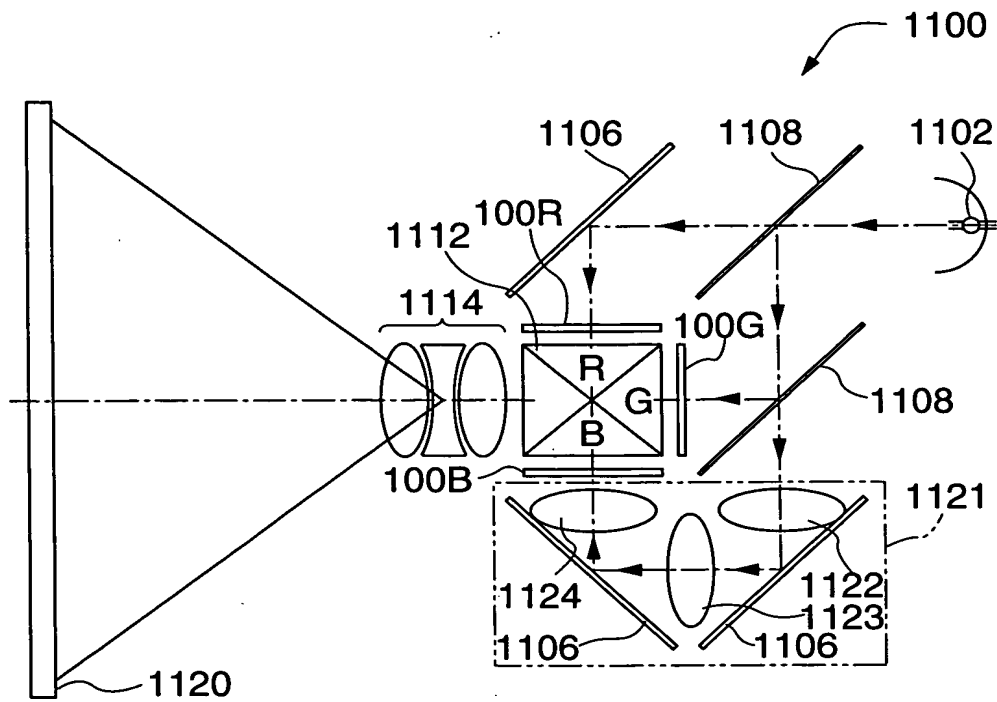


FIG. 14

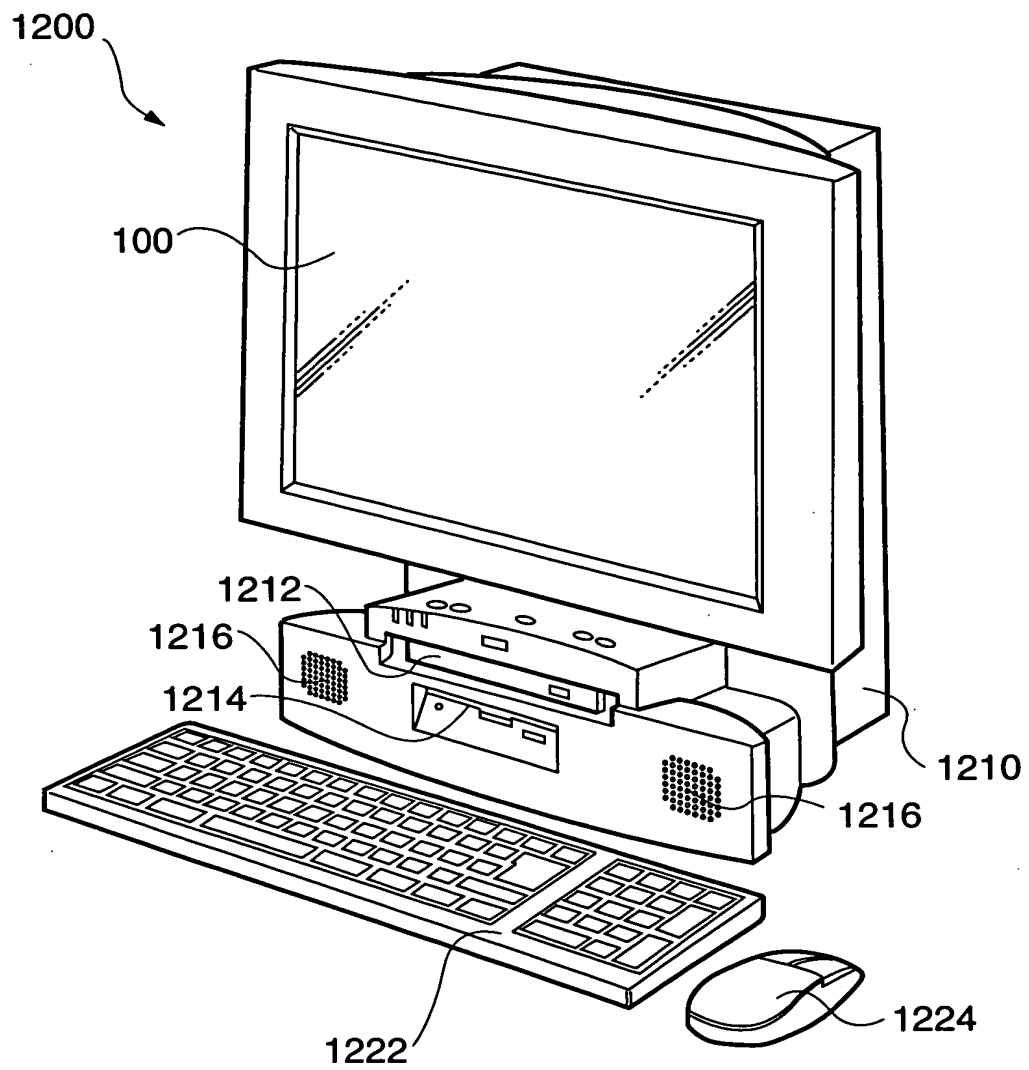


FIG. 15

1004822.120701

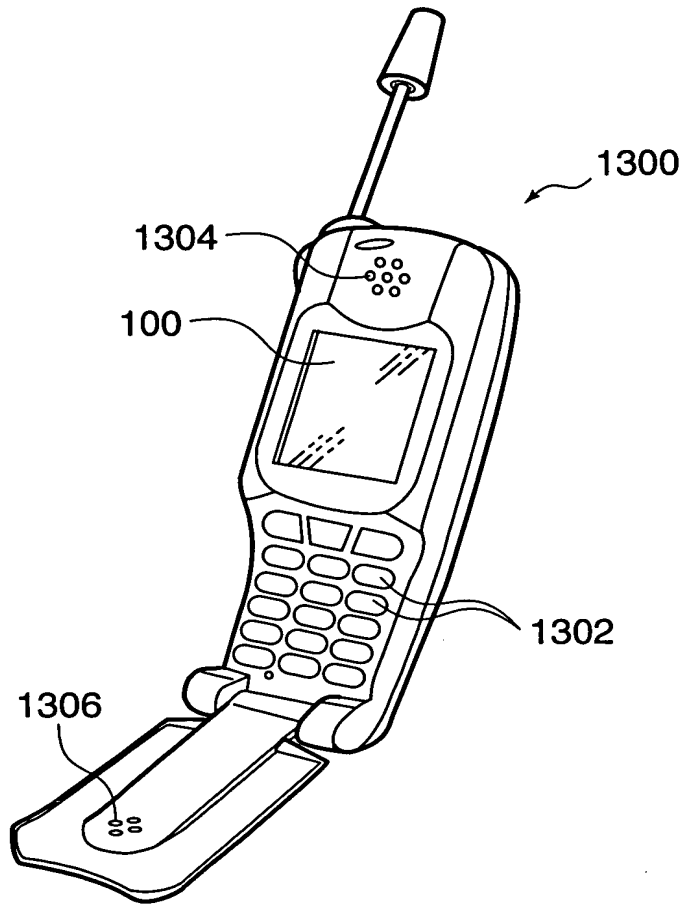


FIG. 16